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architecture-dependent micro optimization
Jochen Liedtke , Kevin Elphinstone
ACM SIGOPS Operating Systems Review January 1996
Volume 30 Issue 1
> Guarded Page Tables implement huge sparsely occupied address spaces efficiently and have the advantages of multi-level tables (tree structure, hierarchy, sharing). We present an implementation guarded page tables on the R4600 processor. The paper describes both the architecture-dependent design process of the algorithms and the resulting tool box.

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Hawaii International Conference on , 3-6 Jan 1995
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Channon, D.; Koch, D.;
System Sciences, 1997, Proceedings of the Thirtieth Hwaii
International Conference on , Volume: 5 , 7-10 Jan 1997
Page(s): 168 -177 vol.5

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Teller, P.J.;
Computer , Volume: 23 Issue: 6 , Jun 1990
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